# Lab 1 scoring key

Please note how many hours each student spent on the excel sheet

Points:

[1] reporting time

[2] for correct truth table

[1] for reasonable logic implementation using no more than 3 ANDs and 2 ORs

[2] for schematic

[2] for correct simulation

[1] verifying circuit worked correctly on DE2 board

[1] verifying circuit worked correctly on breadboard

If the simulation waveforms contain a small number box corresponding to a gray square in simulation where the output was not determined, caused by an unconnected wire in the schematic, only take off two points between simulation and schematic.

If truth table is incorrect but is implemented properly, only take off 2 points for bad truth table.

If lab is disorganized or hard to read, take off one to two points depending on how painful it is to grade.